

Advanced Engineering Course Organized at
The Hong Kong University of Science and Technology

ADVANCED ANALOG IC DESIGN

January 6 - 9, 2010

Wednesday, January 6

Willy Sansen,
KU Leuven

- Noise Performance of Elementary Transistor Stages
- Stability of Operational Amplifiers
- Systematic Design of Low-Power Operational Amplifiers
- Important Opamp Configurations

Thursday, January 7

Gabor Temes,
Oregon State University

- Advanced SC Circuit Design Techniques

Willy Sansen,
KU Leuven

- Fully-Differential Operational Amplifiers
- Offset and CMRR: Random and Systematic

Friday, January 8

Gabor Temes,
Oregon State University

- Non-ideal Effects in SC Circuits
- Thermal Noise Estimation in SC Circuits

Willy Sansen,
KU Leuven

- Distortion in Elementary Transistor Circuits
- Continuous-Time Filters

Saturday, January 9

Gabor Temes,
Oregon State University

- Micro-Power A/D Converters

Wing-Hung Ki,
HKUST

- Bandgap References

Deadline for Registration: DECEMBER 4, 2009

Local address: HKUST, Clear Water Bay, Kowloon, Hong Kong

Contact & Registration address: MEAD Education SA, Ch. de la Venoge 7, 1025 St-Sulpice, Switzerland
Tel: +41-21-695-2222, Fax: +41-21-695-2220

e-mail: valence@mead.ch (technical) or education@mead.ch (administrative)

More information on our Web page: www.mead.ch