VDDA

VDDD

1/20/20
4000

1/20/20
4000

1/20/20
4000

1/20/20
4000

1/20/20
4000

1/20/20
4000

A

Analog I/O

D

1/20/20
4000

1/20/20
4000

1/20/20
4000

Digital I/O

DOUT

1/20/20
4000
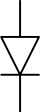
1/20/20
4000

1/20/20
4000

1/20/20
4000

1/20/20
4000

GNDD

GNDA

Note1:   Protection Diode made of NMOS   Protection Diode made of PMOS

Note2:   1/20/20
4000   Where :1/20/20=1um GateL,20um GateW snd 20 Gate Fingers of MOS; 4000 = tolerate 4000V