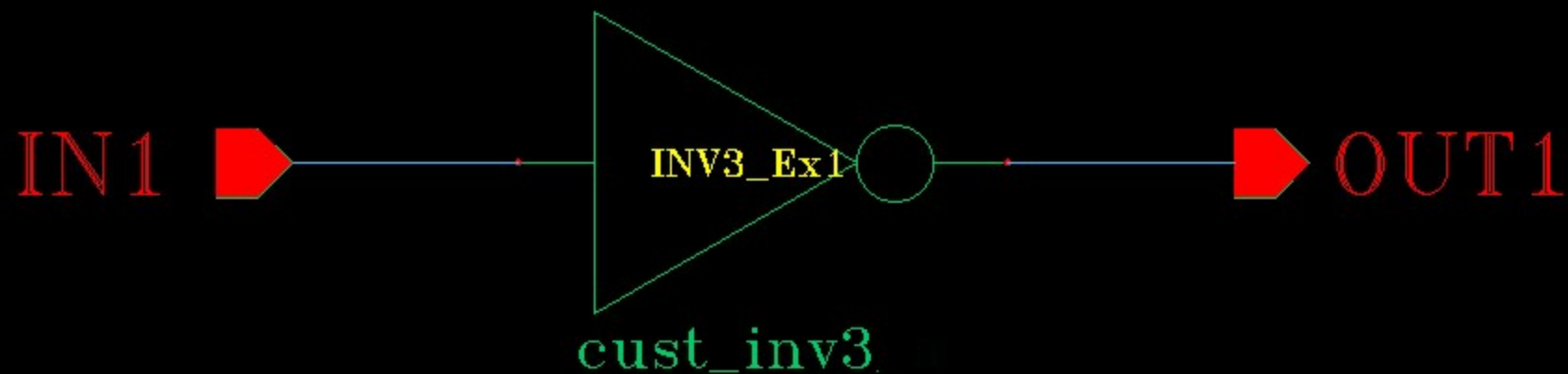


V1P2D

VSUB = VBLK  
VNEG = DGND  
VPOS = V1P2D



DGND

VBLK

**Example-1**

V5D ►

VSUB = VBLK  
VNEG = V5M1P2D  
VPOS = V5D



V5M1P2D ►

VBLK ►

**Example-2**