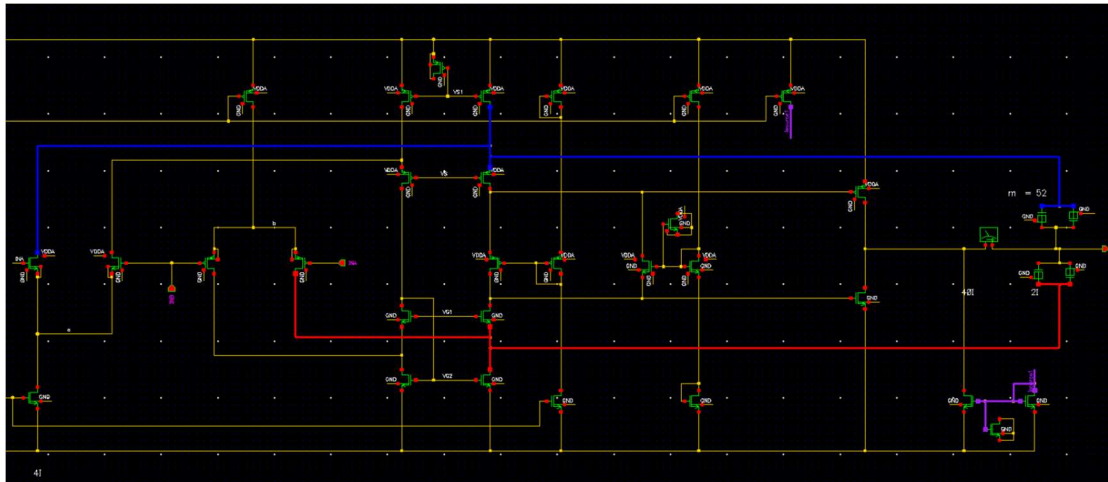
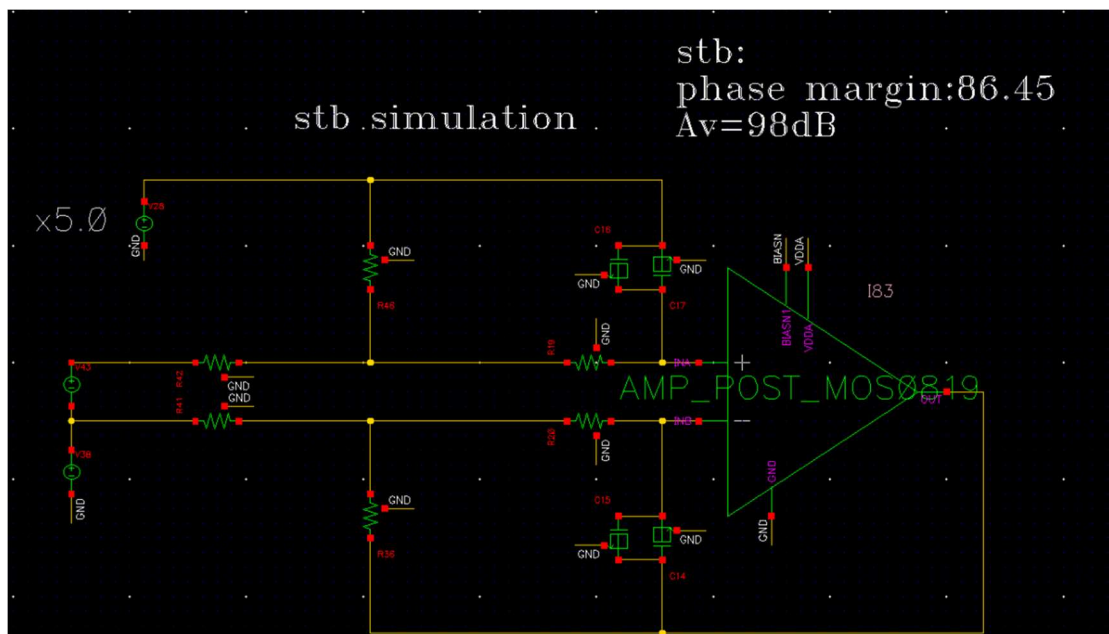


Problems

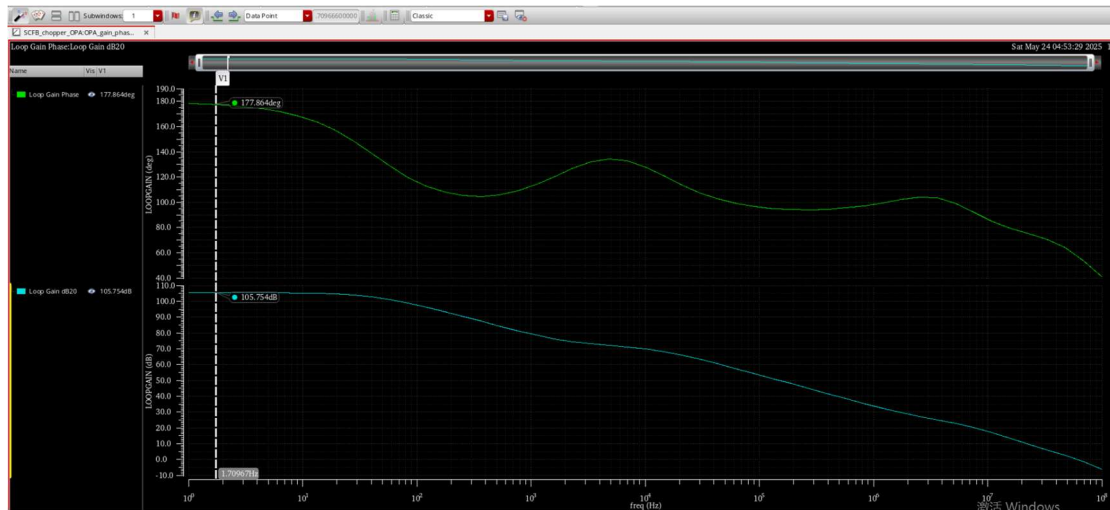
1. OPA Circuit Architecture



2. OPA Peripheral Configuration



3.Simulation Results When $V_{DD} < 5V$



4.Simulation Results When $V_{DD} > 5V$

