

Expressions

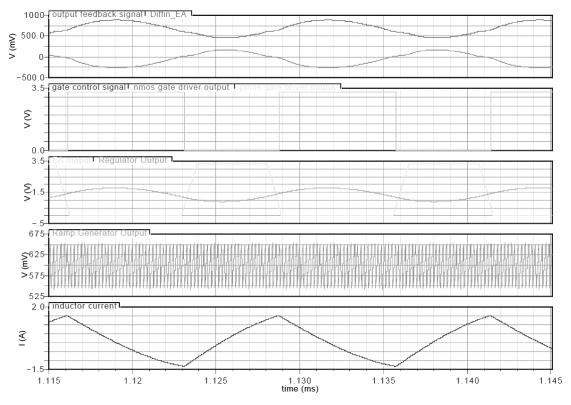


Figure 2 ideal error amplifier with compensation network