```
// Generated for: spectre
// Generated on: Apr 28 11:19:40 2006
// Design library name: uwb_18
// Design cell name: ring_oscillator_res
// Design view name: schematic
simulator lang=spectre
global 0
include "/cadence/ic-
v5033/tools.sun4v/dfII/samples/artist/ahdlLib/quantity.spectre"
include "/pe_users/guest12/models/mm18twinwellv121.lib.scs" section=tt

// Library name: uwb_18
// Cell name: ring_oscillator_res
// View name: schematic
V0 (net055 0) vsource dc=1.8
R3 (net055 net042) resistor r=1k
R0 (net055 net24) resistor r=1k
R1 (net055 net20) resistor r=1k
R5 (net055 net039) resistor r=1k
R2 (net055 net19) resistor r=1k
R4 (net055 net044) resistor r=1k
I0 (net055 net037) isource dc=1m
M3 (net20 net039 net25 net25) N_18_MM w=40u ad=1.96e-11 as=1.96e-11 \
        pd=80.98u ps=80.98u
M4 (net042 net24 net17 net17) N_18_MM w=40u ad=1.96e-11 as=1.96e-11 \
        pd=80.98u ps=80.98u
M5 (net039 net19 net054 net054) N_18_MM w=40u ad=1.96e-11 as=1.96e-11 \
        pd=80.98u ps=80.98u
M7 (net17 net037 0 0) N_18_MM w=80u l=0.4u ad=3.92e-11 as=3.92e-11 \
        pd=160.98u ps=160.98u
M8 (net054 net037 0 0) N_18_MM w=80u l=0.4u ad=3.92e-11 as=3.92e-11 \
        pd=160.98u ps=160.98u
M9 (net037 net037 0 0) N_18_MM w=80u l=0.4u ad=3.92e-11 as=3.92e-11 \
        pd=160.98u ps=160.98u
M6 (net25 net037 0 0) N_18_MM w=80u l=0.4u ad=3.92e-11 as=3.92e-11 \
        pd=160.98u ps=160.98u
M0 (net24 net044 net25 net25) N_18_MM w=40u ad=1.96e-11 as=1.96e-11 \
        pd=80.98u ps=80.98u
M1 (net19 net20 net17 net17) N_18_MM w=40u ad=1.96e-11 as=1.96e-11 \
        pd=80.98u ps=80.98u
M2 (net044 net042 net054 net054) N_18_MM w=40u ad=1.96e-11 as=1.96e-11 \
        pd=80.98u ps=80.98u
C11 (net039 0) capacitor c=4.7p
C4 (net20 0) capacitor c=4.7p
C8 (net19 0) capacitor c=4.7p
C6 (net24 0) capacitor c=4.7p
C5 (net042 0) capacitor c=4.7p
C0 (net044 0) capacitor c=4.7p ic=0
simulatorOptions options reltol=1e-3 vabstol=1e-6 iabstol=1e-12 temp=27 \
    tnom=27 scalem=1.0 scale=1.0 gmin=1e-12 rforce=1 maxnotes=5 maxwarns=5 \
    digits=5 cols=80 pivrel=1e-3 ckptclock=1800 \
    sensfile="../psf/sens.output"
tran tran stop=1u write="spectre.ic" writefinal="spectre.fc" \
    annotate=status maxiters=5
finalTimeOP info what=oppoint where=rawfile
dcOp dc write="spectre.dc" maxiters=150 maxsteps=10000 annotate=status
capInfo_dc info what=captab where=logfile threshold=0.0 \
```

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        detail=nodetoground sort=name
dcOpInfo info what=oppoint where=rawfile
modelParameter info what=models where=rawfile
element info what=inst where=rawfile
outputParameter info what=output where=rawfile
designParamVals info what=parameters where=rawfile
saveOptions options save=allpub
```